

M113A

Mini PCIe 32 GPIO extension module

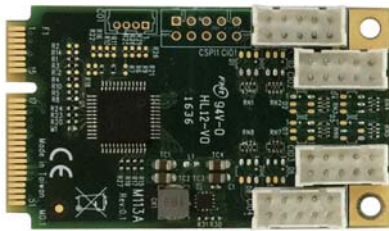
Introduction:

GPIO extension board with F75113 chipset.

F75113 is a low power general purpose IO chip provides 40 GPIO.

Level /pulse mode can be programmed by registers. All GPIO can be programmed to logic one, zero, high pulse or low pulse. GPIO0X~GPIO2X can be programmed to power LED.

TOP



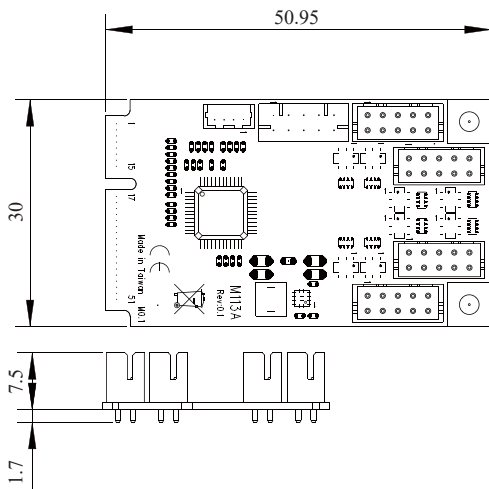
Features:

- Low Pin Count or SMBus Interface to 32 GPIO pins

Specification:

Type	32 GPIO
Interface	SMBus, LPC
GPIO	4*2 x 5 pin wafer Work with CIO208, CIO108, CIO116
Operating Temperature	-40°C ~ +85°C
Dimensions	50.95 x 30 mm

Mechanical Drawing:



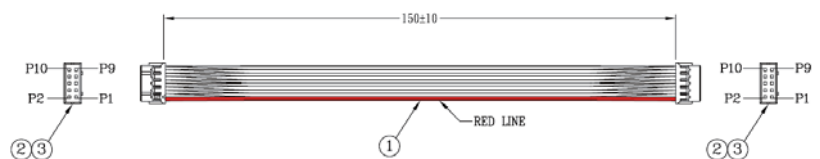
Ordering Information:

7G5905-1529001-0 M113A-L-001 (LPC)

7G5905-1530001-0 M113A-S-001 (SMBus)

Cable Information:

6G6001-0105-0100 L=15cm, PHD2.0 2 x 5 / 2 x 5, 31525



Digital Input / output board option:

• CIO116:

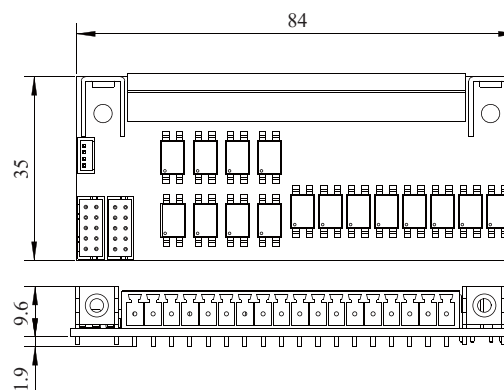
8 DI+8DO: M113A+CIO116

16DI+16DO: M113A+CIO116 x 2



CIO116-B-001: 7G5905-1802001-0

Mechanical Drawing:



Digital Input / output board option:

• CIO208:

8DI: M113A+CIO208-8I

8DO: M113A+CIO208-8O

4DI 4DO: M113A+CIO208-4DI4DO

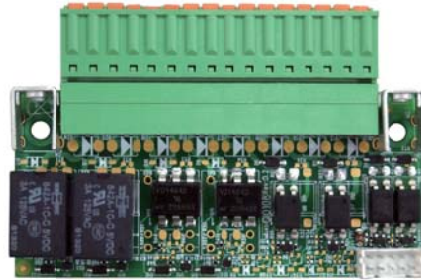


7G5905-1711002-0 CIO208-4I4OI-002
7G5905-1712002-0 CIO208-8I-002
7G5905-1710002-0 CIO208-8O-002

• CIO108:

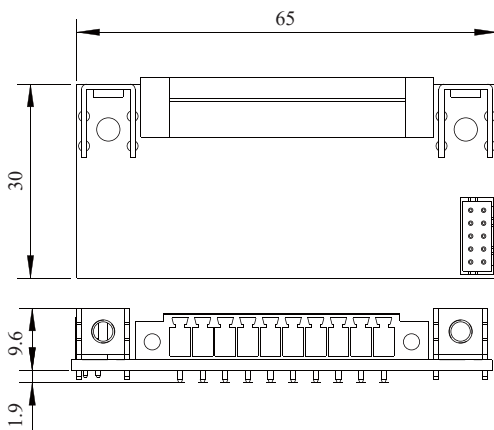
4DI 4DO: M113A+CIO108-4DI4DO

(4 SSR DI / 2 SSR DO+2 delay DO)



7G5905-1701002-0 CIO108-4I4O-12V-002

Mechanical Drawing:



Mechanical Drawing:

